

# **Linux 2.4.x Initialization for IA-32 HOWTO**

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# Linux 2.4.x Initialization for IA-32 HOWTO

Randy Dunlap, [rddunlap@ieee.org](mailto:rddunlap@ieee.org)

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*This document contains a description of the Linux 2.4 kernel initialization sequence on IA-32 processors.*

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### 1. [Introduction](#)

Portions of this text come from comments in the kernel source files (obviously). I have added annotations in many places. I hope that this will be useful to kernel developers — either new ones or experienced ones who need more of this type of information. However, if there's not enough detail here for you, "Use the Source."

#### 1.1 Overview

This description is organized as a brief overview which lists the sections that are described later in more detail.

The description is in three main sections. The first section covers early kernel initialization on IA-32 (but only after your boot loader of choice and other intermediate loaders have run; i.e., this description does not cover loading the kernel). This section is based on the code in "linux/arch/i386/boot/setup.S" and "linux/arch/i386/boot/video.S".

The second major section covers Linux initialization that is x86- (or i386- or IA-32-) specific. This section is based on the source files "linux/arch/i386/kernel/head.S" and "linux/arch/i386/kernel/setup.c".

The third major section covers Linux initialization that is architecture-independent. This section is based on the flow in the source file "linux/init/main.c".

See the References section for other valuable documents about booting, loading, and initialization.

#### 1.2 This document

This document describes Linux 2.4.x initialization on IA-32 (or i386 or x86) processors — after one or more kernel boot loaders (if any) have done their job.

You can format it using the commands (for example):

```
% sgm12txt ia32_init_240.sgml
```

or

```
% sgm12html ia32_init_240.sgml
```

This will produce plain ASCII or HTML files respectively. You can also produce LaTeX, GNU, and RTF info by using the proper sgm1tool (man sgm1tools).

## 1.3 Contributions

Additions and corrections are welcome. Please send them to me (rddunlap@ieee.org). Contributions of section descriptions that are used will be credited to their author(s).

## 1.4 Trademarks

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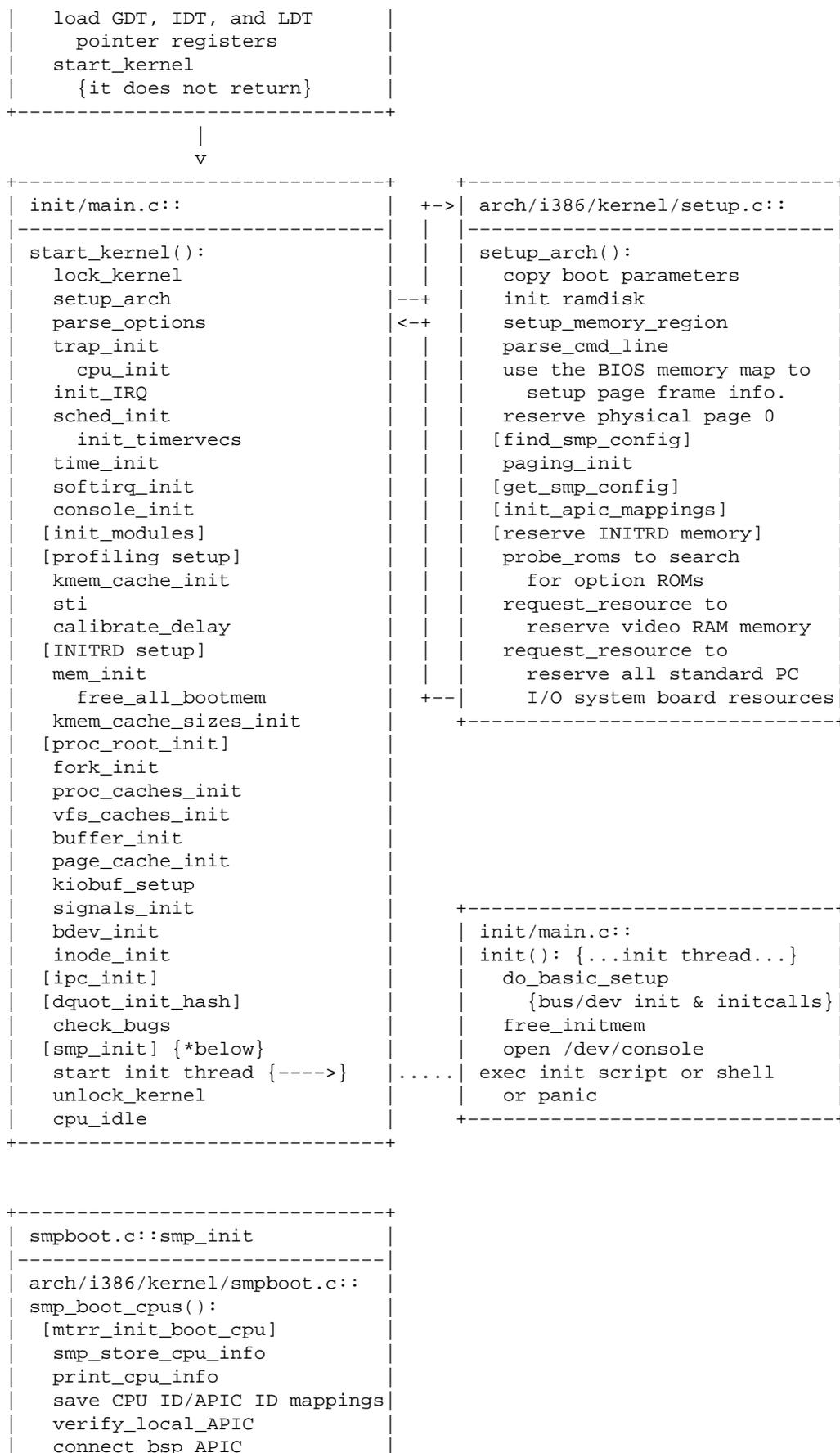
---

## 2. Linux init ("ASCII art")

Pictorially (loosely speaking :), Linux initialization looks like this, where "[...]" means optional (depends on the kernel's configuration) and "{...}" is a comment.

```
+-----+
| arch/i386/boot/setup.S:: +
| arch/i386/boot/video.S::
+-----+
| start_of_setup:
|   check that loaded OK
|   get system memory size
|   get video mode(s)
|   get hard disk parameters
|   get MC bus information
|   get mouse information
|   get APM BIOS information
|   enable address line A20
|   reset coprocessor
|   mask all interrupts
|   move to protected mode
|   jmp to startup_32
+-----+
|
| v
+-----+
| arch/i386/kernel/head.S::
+-----+
| startup_32:
|   set segment registers to
|   known values
|   init basic page tables
|   setup the stack pointer
|   clear kernel BSS
|   setup the IDT
|   checkCPUtype
+-----+
```

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```

|  setup_local_APIC          |
|  foreach valid APIC ID    |
|    do_boot_cpu(apicid)   |
|  setup_IO_APIC           |
|  setup_APIC_clocks       |
|  synchronize_tsc_bp      |
+-----+

```

---

### 3. [Linux early setup](#)

(from linux/arch/i386/boot/setup.S and linux/arch/i386/boot/video.S)

NOTE: Register notation is %regname and constant notation is a number, with or without a leading '\$' sign.

#### 3.1 IA-32 Kernel Setup

"setup.S" is responsible for getting the system data from the BIOS and putting them into the appropriate places in system memory.

Both "setup.S" and the kernel have been loaded by the boot block.

"setup.S" is assembled as 16-bit real-mode code. It switches the processor to 32-bit protected mode and jumps to the 32-bit kernel code.

This code asks the BIOS for memory/disk/other parameters, and puts them in a "safe" place: 0x90000-0x901FF, that is, where the boot block used to be. It is then up to the protected mode system to read them from there before the area is overwritten for buffer-blocks.

The "setup.S" code begins with a jmp instruction around the "setup header", which must begin at location %cs:2.

This is the setup header:

---

```

                                .ascii  "HdrS"          # header signature
                                .word   0x0202          # header version number
realmode_swth:                  .word   0, 0           # default_switch, SETUPSEG
start_sys_seg:                  .word   SYSSEG
                                .word   kernel_version # pointer to kernel version string
type_of_loader:                 .byte   0
loadflags:
LOADED_HIGH                     = 1                 # If set, the kernel is loaded high
#ifdef __BIG_KERNEL__
                                .byte   0
#else
                                .byte   LOADED_HIGH
#endif
setup_move_size:                .word   0x8000        # size to move, when setup is not
                                # loaded at 0x90000.
code32_start:                   # here loaders can put a different
                                # start address for 32-bit code.
#ifdef __BIG_KERNEL__
                                .long   0x1000        # default for zImage

```

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```
#else
        .long    0x100000# default for big kernel
#endif
ramdisk_image: .long    0        # address of loaded ramdisk image
ramdisk_size:  .long    0        # its size in bytes
bootsect_kludge: .word    bootsect_helper, SETUPSEG
heap_end_ptr:   .word    modelist+1024 # (Header version 0x0201 or later)
                                     # space from here (exclusive) down to
                                     # end of setup code can be used by setup
                                     # for local heap purposes.

pad1:          .word    0
cmd_line_ptr:  .long    0        # (Header version 0x0202 or later)
                                     # If nonzero, a 32-bit pointer
                                     # to the kernel command line.

trampoline:   call    start_of_setup # no return from start_of_setup
               .space  1024
# End of setup header #####
```

---

### start\_of\_setup:

#### Read second hard drive DASD type

Read the DASD type of the second hard drive (BIOS int. 0x13, %ax=0x1500, %dl=0x81).

# Bootlin depends on this being done early. [TBD:why?]

#### Check that LILO loaded us right

Check the signature words at the end of setup. Signature words are used to ensure that LILO loaded us right. If the two words are not found correctly, copy the setup sectors and check for the signature words again. If they still aren't found, panic("No setup signature found ...").

#### Check old loader trying to load a big kernel

If the kernel image is "big" (and hence is "loaded high"), then if the loader cannot handle "loaded high" images, then panic ("Wrong loader, giving up...").

#### Determine system memory size

Get the extended memory size {above 1 MB} in KB. First clear the extended memory size to 0.

```
#ifndef STANDARD_MEMORY_BIOS_CALL
```

Clear the E820 memory area counter.

Try three different memory detection schemes.

First, try E820h, which lets us assemble a memory map, then try E801h, which returns a 32-bit memory size, and finally 88h, which returns 0-64 MB.

Method E820H populates a table in the empty\_zero\_block that contains a list of usable address/size/type tuples. In "linux/arch/i386/kernel/setup.c", this information is transferred into the e820map, and in

start\_of\_setup:

"linux/arch/i386/mm/init.c", that new information is used to mark pages reserved or not.

### Method E820H:

Get the BIOS memory map. E820h returns memory classified into different types and allows memory holes. We scan through this memory map and build a list of the first 32 memory areas {up to 32 entries or BIOS says that there are no more entries}, which we return at "E820MAP". [See URL: <http://www.teleport.com/acpi/acpihtml/topic245.htm>]

### Method E801H:

We store the 0xe801 memory size in a completely different place, because it will most likely be longer than 16 bits.

This is the sum of 2 registers, normalized to 1 KB chunk sizes: %ecx = memory size from 1 MB to 16 MB range, in 1 KB chunks + %edx = memory size above 16 MB, in 64 KB chunks.

### Ye Olde Traditional Methode:

BIOS int. 0x15/AH=0x88 returns the memory size (up to 16 MB or 64 MB, depending on the BIOS). We always use this method, regardless of the results of the other two methods.

#endif

Set the keyboard repeat rate to the maximum rate using using BIOS int. 0x16.

## Video adapter modes

Find the video adapter and its supported modes and allow the user to browse video modes.

call video # {see Video section below}

## Get Hard Disk parameters

Get hd0 data: Save the hd0 descriptor (from int. vector 0x41) at INITSEG:0x80 length 0x10.

Get hd1 data: Save the hd1 descriptor (from int. vector 0x46) at INITSEG:0x90 length 0x10.

Check that there IS an hd1, using BIOS int. 0x13. If not, clear its descriptor.

## Get Micro Channel bus information

Check for Micro Channel (MCA) bus:

- Set MCA feature table length to 0 in case not found.
- Get System Configuration Parameters (BIOS int. 0x15/%ah=0xc0). This sets %es:%bx to point to the system feature table.
- We keep only the first 16 bytes of the system feature table if found: Structure size, Model byte, Submodel byte, BIOS revision, and Feature information bytes 1–5. Bit 0 or 1 (either one) of Feature byte 1 indicates that the system contains a Micro Channel bus.

## Check for mouse

Check for PS/2 pointing device by using BIOS int. 0x11 {get equipment list}.

- Clear the pointing device flag (default).
- BIOS int. 0x11: get equipment list.
- If bit 2 (value 0x04) is set, then a mouse is installed and the pointing device flag is set to indicate that the device is present.

## Check for APM BIOS support

Check for an APM BIOS (if kernel is configured for APM support):

- start: clear version field to 0, which means no APM BIOS present.
- Check for APM BIOS installation using BIOS int. 0x15.
- If not present, done.
- Check for "PM" signature returned in %bx.
- If no signature, then no APM BIOS: done.
- Check for 32-bit support in %cx.
- If no 32-bit support, no (good) APM BIOS: done. Must have 32-bit APM BIOS support to be used by Linux.
- Save the BIOS code segment, BIOS entry point offset, BIOS 16-bit code segment, BIOS data segment, BIOS code segment length, and BIOS data segment length.
- Record the APM BIOS version and flags.

## Prepare to move to protected mode

We build a jump instruction to the kernel's code32\_start address. (The loader may have changed it.)

Move the kernel to its correct place if necessary.

Load the segment descriptors (load %ds = %cs).

Make sure that we are at the right position in memory, to accommodate the command line and boot parameters at their fixed locations.

Load the IDT pointer register with 0,0.

Calculate the linear base address of the kernel GDT (table) and load the GDT pointer register with its base address and limit. This early kernel GDT describes kernel code as 4 GB, with base address 0, code/readable/executable, with granularity of 4 KB. The kernel data segment is described as 4 GB, with base address 0, data/readable/writable, with granularity of 4 KB.

## Enable address line A20

- Empty the 8042 (keyboard controller) of any queued keys.
- Write 0xd1 (Write Output Port) to Command Register port 0x64.
- Empty the 8042 (keyboard controller) of any queued keys.
- Write 0xdf (Gate A20 + more) to Output port 0x60.
- Empty the 8042 (keyboard controller) of any queued keys.

- Set bit number 1 (value 0x02: FAST\_A20) in the "port 0x92" system control register. This enables A20 on some systems, depending on the chipset used in them.
- Wait until A20 really *is* enabled; it can take a fair amount of time on certain systems. The memory location used here (0x200) is the int 0x80 vector, which should be safe to use. When A20 is disabled, the test memory locations are an alias of each other (segment 0:offset 0x200 and segment 0xffff:offset 0x210). {0xffff0 + 0x210 = 0x100200, but if A20 is disabled, this becomes 0x000200.} We just wait (busy wait/loop) until these memory locations are no longer aliased.

## Make sure any possible coprocessor is properly reset

- Write 0 to port 0xf0 to clear the Math Coprocessor '-busy' signal.
- Write 0 to port 0xf1 to reset the Math Coprocessor.

## Mask all interrupts

Now we mask all interrupts; the rest is done in `init_IRQ()`.

- Mask off all interrupts on the slave PIC: write 0xff to port 0xa1.
- Mask off all interrupts on the master PIC except for IRQ2, which is the cascaded IRQ input from the slave PIC: write 0xfb to port 0x21.

## Move to Protected Mode

Now is the time to actually move into protected mode. To make things as simple as possible, we do no register setup or anything, we let the GNU-compiled 32-bit programs do that. We just jump to absolute address 0x1000 (or the loader supplied one), in 32-bit protected mode.

Note that the short jump isn't strictly needed, although there are reasons why it might be a good idea. It won't hurt in any case.

Set the PE (Protected mode Enable) bit in the MSW and jump to the following instruction to flush the instruction fetch queue.

Clear %bx to indicate that this is the BSP (first CPU only).

## Jump to startup\_32 code

Jump to the 32-bit kernel code (`startup_32`).

NOTE: For high-loaded big kernels we need:

```
jmp    0x100000, __KERNEL_CS
```

but we yet haven't reloaded the %cs register, so the default size of the target offset still is 16 bit. However, using an operand prefix (0x66), the CPU will properly take our 48-bit far pointer. (INTEL 80386 Programmer's Reference Manual, Mixing 16-bit and 32-bit code, page 16-6).

```
code32: .byte 0x66, 0xea          # prefix + jmp opcode
        .long 0x1000            # or 0x100000 for big kernels
        .word  __KERNEL_CS
```

This jumps to "startup\_32" in "linux/arch/i386/kernel/head.S".

## 3.2 Video Setup

"linux/arch/i386/boot/video.S" is included into "linux/arch/i386/boot/setup.S", so they are assembled together. The file separation is a logical module separation even though the two modules aren't built separately.

"video.S" handles Linux/i386 display adapter and video mode setup. For more information about Linux/i386 video modes, see "linux/Documentation/svgas.txt" by Martin Mares [mj@ucw.cz].

Video mode selection is a kernel build option. When it is enabled, You can select a specific (fixed) video mode to be used during kernel booting or you can ask to view a selection menu and then choose a video mode from that menu.

There are a few esoteric (!) "video.S" build options that not covered here. See "linux/Documentation/svgas.txt" for all of them.

CONFIG\_VIDEO\_SVGA (for automatic detection of SVGA adapters and modes) is normally #undefined. The normal method of video adapter detection on Linux/i386 is VESA (CONFIG\_VIDEO\_VESA, for autodetection of VESA modes).

"video:" is the main entry point called by "setup.S". The %ds register \*must\* be pointing to the bootsector. The "video.S" code uses different segments from the main "setup.S" code.

This is a simplified description of the code flow in "video.S". It does not address the CONFIG\_VIDEO\_LOCAL, CONFIG\_VIDEO\_400\_HACK, and CONFIG\_VIDEO\_GFX\_HACK build options and it does not dive deep into video BIOS calls or video register accesses.

### video:

- %fs is set to the original %ds value
- %ds and %es are set to %cs
- %gs is set to zero
- Detect the video adapter type and supported modes. (call basic\_detect)
- #ifdef CONFIG\_VIDEO\_SELECT
- If the user wants to see a list of the supported VGA adapter modes, list them. (call mode\_menu)
- Set the selected video mode. (call mode\_set)
- #ifdef CONFIG\_VIDEO\_RETAIN
- Restore the screen contents. (call restore\_screen)
- #endif /\* CONFIG\_VIDEO\_RETAIN \*/
- #endif /\* CONFIG\_VIDEO\_SELECT \*/
- Store mode parameters for kernel. (call mode\_params)
- Restore original DS register value.

### basic\_detect:

- Detect if we have CGA, MDA, HGA, EGA, or VGA and pass it to the kernel.
- Check for EGA/VGA using BIOS int. 0x10 calls. This also tells whether the video adapter is CGA/MDA/HGA.

- The "adapter" variable is returned as 0 for CGA/MDA/HGA, 1 for EGA, and 2 for VGA.

## **mode\_params:**

- Store the video mode parameters for later use by the kernel. This is done by asking the BIOS for mode parameters except for the rows/columns parameters in the default 80x25 mode -- these are set directly, because some very obscure BIOSes supply insane values.
- `#ifdef CONFIG_VIDEO_SELECT`
- For graphics mode with a linear frame buffer, goto `mopar_gr`.
- `#endif /* CONFIG_VIDEO_SELECT */`
- For MDA/CGA/HGA/EGA/VGA:
- Read and save cursor position.
- Read and save video page/mode/width.
- For MDA/HGA, change the `video_segment` to `$0xb000`. (Leave it at its initial value of `$0xb800` for all other adapters.)
- Get the Font size (valid only on EGA/VGA).
- Save the number of video columns and lines.

`#ifdef CONFIG_VIDEO_SELECT`

## **mopar\_gr:**

- Get VESA frame buffer parameters.
- Get video mem size and protected mode interface information using BIOS int. 0x10 calls.

## **mode\_menu:**

Build the mode list table and display the mode menu.

## **mode\_set:**

For the selected video mode, use BIOS int. 0x10 calls or register writes as needed to set some or all of:

- Reset the video mode
- Number of scan lines
- Font pixel size
- Save the screen size in `force_size`. "force\_size" is used to override possibly broken video BIOS interfaces and is used instead of the BIOS variables.

Some video modes require register writes to set:

- Location of the cursor scan lines
- Vertical sync start
- Vertical sync end
- Vertical display end
- Vertical blank start
- Vertical blank end
- Vertical total
- (Vertical) overflow
- Correct sync polarity

`mode_params:`

- Preserve clock select bits and color bit

{end of mode\_set}

```
#ifdef CONFIG_VIDEO_RETAIN /* Normally _IS_ #defined */
```

### **store\_screen:**

CONFIG\_VIDEO\_RETAIN is used to retain screen contents when switching modes. This option stores the screen contents to a temporary memory buffer (if there is enough memory) so that they can be restored later.

- Save the current number of video lines and columns, cursor position, and video mode.
- Calculate the image size.
- Save the screen image.
- Set the "do\_restore" flag so that the screen contents will be restored at the end of video mode detection/selection.

### **restore\_screen:**

Restores screen contents from temporary buffer (if already saved).

- Get parameters of current mode.
- Set cursor position.
- Restore the screen contents.

```
#endif /* CONFIG_VIDEO_RETAIN */
```

### **mode\_table:**

Build the table of video modes at `modelist`.

- Store standard modes.
- Add modes for standard VGA.
- #ifdef CONFIG\_VIDEO\_LOCAL
- Add locally-defined video modes. (call local\_modes)
- #endif /\* CONFIG\_VIDEO\_LOCAL \*/
- #ifdef CONFIG\_VIDEO\_VESA
- Auto-detect VESA VGA modes. (call vesa\_modes)
- #endif /\* CONFIG\_VIDEO\_VESA \*/
- #ifdef CONFIG\_VIDEO\_SVGA
- Detect SVGA cards & modes. (call svga\_modes)
- #endif /\* CONFIG\_VIDEO\_SVGA \*/
- #ifdef CONFIG\_VIDEO\_COMPACT
- Compact the video modes list, removing duplicate entries.
- #endif /\* CONFIG\_VIDEO\_COMPACT \*/

### **mode\_scan:**

Scans for video modes.

store\_screen:

- Start with mode 0.
- Test the mode.
- Test if it's a text mode.
- OK, store the mode.
- Restore back to mode 3.

```
#ifdef CONFIG_VIDEO_SVGA
```

### **svga\_modes:**

Try to detect the type of SVGA card and supply (usually approximate) video mode table for it.

- Test all known SVGA adapters.
- Call the test routine for each adapter.
- If adapter is found, copy the video modes.
- Store pointer to card name.

```
#endif /* CONFIG_VIDEO_SVGA */
```

```
#endif /* CONFIG_VIDEO_SELECT */
```

---

## **4. Linux architecture-specific initialization**

(from "linux/arch/i386/kernel/head.S")

The boot code in "linux/arch/i386/boot/setup.S" transfers execution to the beginning code in "linux/arch/i386/kernel/head.S" (labeled "startup\_32:").

To get to this point, a small uncompressed kernel function decompresses the remaining compressed kernel image and then it jumps to the new kernel code.

This is a description of what the "head.S" code does.

### **4.1 startup\_32:**

swapper\_pg\_dir is the top-level page directory, address 0x00101000.

On entry, %esi points to the real-mode code as a 32-bit pointer.

### **4.2 Set segment registers to known values**

Set the %ds, %es, %fs, and %gs registers to \_\_KERNEL\_DS.

### **4.3 SMP BSP (Bootstrap Processor) check**

```
#ifdef CONFIG_SMP
```

```
svga_modes:
```

If %bx is zero, this is a boot on the Bootstrap Processor (BSP), so skip this. Otherwise, for an AP (Application Processor):

If the desired %cr4 setting is non-zero, turn on the paging options (PSE, PAE, ...) and skip "Initialize page tables" (jump to "Enable paging").

```
#endif /* CONFIG_SMP */
```

## 4.4 Initialize page tables

Begin at pg0 (page 0) and init all pages to 007 (PRESENT + RW + USER).

## 4.5 Enable paging

Set %cr3 (page table pointer) to swapper\_pg\_dir.

Set the paging ("PG") bit of %cr0 to  
\*\*\*\*\* enable paging \*\*\*\*\*.

Jump \$ to flush the prefetch queue.

Jump \*[\$] to make sure that %eip is relocated.

Setup the stack pointer (lss stack\_start, %esp).

```
#ifdef CONFIG_SMP
```

If this is not the BSP (Bootstrap Processor), clear all flags bits and jump to checkCPUtype.

```
#endif /* CONFIG_SMP */
```

## 4.6 Clear BSS

The BSP clears all of BSS (area between \_\_bss\_start and \_end) for the kernel.

## 4.7 32-bit setup

Setup the IDT for 32-bit mode (call setup\_idt). setup\_idt sets up an IDT with 256 entries pointing to the default interrupt handler "ignore\_int" as interrupt gates. It doesn't actually load the IDT; that can be done only after paging has been enabled and the kernel moved to PAGE\_OFFSET. Interrupts are enabled elsewhere, when we can be relatively sure everything is OK.

Clear the eflags register (before switching to protected mode).

## 4.8 Copy boot parameters and command line out of the way

First 2 KB of \_empty\_zero\_page is for boot parameters, second 2 KB is for the command line.

## 4.9 checkCPUtype

Initialize X86\_CPUID to -1.

Use Flags register, push/pop results, and CPUID instruction(s) to determine CPU type and vendor: Sets X86, X86\_CPUID, X86\_MODEL, X86\_MASK, and X86\_CAPABILITY. Sets bits in %cr0 accordingly.

Also checks for presence of an 80287 or 80387 coprocessor. Sets X86\_HARD\_MATH if a math coprocessor or floating point unit is found.

## 4.10 Count this processor

For CONFIG\_SMP builds, increment the "ready" counter to keep a tally of the number of CPUs that have been initialized.

## 4.11 Load descriptor table pointer registers

Load GDT with gdt\_descr and IDT with idt\_descr. The GDT contains 2 entries for the kernel (4 GB each for code and data, beginning at 0) and 2 userspace entries (4 GB each for code and data, beginning at 0). There are 2 null descriptors between the userspace descriptors and the APM descriptors.

The GDT also contains 4 entries for APM segments. The APM segments have byte granularity and their bases and limits are set at runtime.

The rest of the gdt\_table (after the APM segments) is space for TSSes and LDTs.

Jump to `__KERNEL_CS:%eip` to cause the GDT to be used. Now in  
**\*\*\*\*\* protected mode \*\*\*\*\***.

Reload all of the segment registers: Set the %ds, %es, %fs, and %gs registers to `__KERNEL_DS`.

```
#ifdef CONFIG_SMP
```

```
Reload the stack pointer segment only (%ss) with __KERNEL_DS.
```

```
#else /* not CONFIG_SMP */
```

```
Reload the stack pointer (%ss:%esp) with stack_start.
```

```
#endif /* CONFIG_SMP */
```

```
Clear the LDT pointer to 0.
```

```
Clear the processor's Direction Flag (DF) to 0 for gcc.
```

## 4.12 Start other processors

For CONFIG\_SMP builds, if this is not the first (Bootstrap) CPU, call `initialize_secondary()`, which does not return. The secondary (AP) processor(s) are initialized and then enter idle state until processes are scheduled

on them.

If this is the first or only CPU, call `start_kernel()`. (see below)

/\* the calls above should never return, but in case they do: \*/

L6: `jmp L6`

---

## 5. [Linux architecture-independent initialization](#)

(from "linux/init/main.c")

"linux/init/main.c" begins execution with the `start_kernel()` function, which is called from "linux/arch/i386/kernel/head.S". `start_kernel()` never returns to its caller. It ends by calling the `cpu_idle()` function.

### 5.1 `start_kernel`:

Interrupts are still disabled. Do necessary setups, then enable them.

Lock the kernel (BKL: big kernel lock).

Print the `linux_banner` string (this string resides in "linux/init/version.c") using `printk()`. NOTE: `printk()` doesn't actually print this to the console yet; it just buffers the string until a console device registers itself with the kernel, then the kernel passes the buffered console log contents to the registered console device(s). There can be multiple registered console devices.

\*\*\*\*\* `printk()` can be called very early because it doesn't actually print to anywhere. It just logs the message to "log\_buf", which is allocated statically in "linux/kernel/printk.c". The messages that are saved in "log\_buf" are passed to registered console devices as they register. \*\*\*\*\*

### More architecture-specific init

Call `setup_arch(&command_line)`:

This performs architecture-specific initializations (details below). Then back to architecture-independent initialization....

The remainder of `start_kernel()` is done as follows for all processor architectures, although several of these function calls are to architecture-specific setup/init functions.

### Continue architecture-independent init

Print the kernel command line.

## Parsing command line options

`parse_options(command_line)`: Parse the kernel options on the command line. This is a simple kernel command line parsing function. It parses the command line and fills in the arguments and environment to `init` (thread) as appropriate. Any command-line option is taken to be an environment variable if it contains the character '='. It also checks for options meant for the kernel by calling `checksetup()`, which checks the command line for kernel parameters, these being specified by declaring them using "`__setup`", as in:

---

```
__setup("debug", debug_kernel);
```

---

This declaration causes the `debug_kernel()` function to be called when the string "debug" is scanned. See "linux/Documentation/kernel-parameters.txt" for the list of kernel parameters.

These options are not given to `init` — they are for internal kernel use only. The default argument list for the `init` thread is {"init", NULL}, with a maximum of 8 command-line arguments. The default environment list for the `init` thread is {"HOME=/", "TERM=linux", NULL}, with a maximum of 8 command-line environment variable settings. In case LILO is going to boot us with default command line, it prepends "auto" before the whole `cmdline` which makes the shell think it should execute a script with such name. So we ignore all arguments entered `_before_init=...` [MJ]

### trap\_init

(in `linux/arch/i386/kernel/traps.c`)

Install exception handlers for basic processor exceptions, i.e., not hardware device interrupt handlers.

Install the handler for the system call software interrupt.

Install handlers for `lcall7` (for iBCS) and `lcall27` (for Solaris/x86 binaries).

Call `cpu_init()` to do:

- initialize per-CPU state
- reload the GDT and IDT
- mask off the eflags NT (Nested Task) bit
- set up and load the per-CPU TSS and LDT
- clear 6 debug registers (0, 1, 2, 3, 6, and 7)
- `stts()`: set the 0x08 bit (TS: Task Switched) in CR0 to enable lazy register saves on context switches

### init\_IRQ

(in `linux/arch/i386/kernel/i8259.c`)

Call `init_ISA_irqs()` to initialize the two 8259A interrupt controllers and install default interrupt handlers for the ISA IRQs.

Set an interrupt gate for all unused interrupt vectors.

For CONFIG\_SMP configurations, set up IRQ 0 early, since it's used before the IO APIC is set up.

For CONFIG\_SMP, install the interrupt handler for CPU-to-CPU IPIs that are used for the "reschedule helper."

For CONFIG\_SMP, install the interrupt handler for the IPI that is used to invalidate TLBs.

For CONFIG\_SMP, install the interrupt handler for the IPI that is used for generic function calls.

For CONFIG\_X86\_LOCAL\_APIC configurations, install the interrupt handler for the self-generated local APIC timer IPI.

For CONFIG\_X86\_LOCAL\_APIC configurations, install interrupt handlers for spurious and error interrupts.

Set the system's clock chip to generate a timer tick interrupt every HZ Hz.

If the system has an external FPU, set up IRQ 13 to handle floating point exceptions.

### **sched\_init**

(in linux/kernel/sched.c)

- Set the init\_task's processor ID.
- Clear the pidhash table. TBD: Why? isn't it in BSS?
- call init\_timervecs()
- call init\_bh() to init "bottom half" queues for timer\_bh, tqueue\_bh, and immediate\_bh.

### **time\_init**

(in linux/arch/i386/kernel/time.c)

Initialize the system's current time of day (xtime) from CMOS.

Install the irq0 timer tick interrupt handler.

### **softirq\_init**

(in linux/kernel/softirq.c)

### **console\_init**

(in linux/drivers/char/tty\_io.c)

HACK ALERT! This is early. We're enabling the console before we've done PCI setups etc., and console\_init() must be aware of this. But we do want output early, in case something goes wrong.

### **init\_modules**

(in linux/kernel/module.c)

For CONFIG\_MODULES configurations, call `init_modules()`. This initializes the size (or number of symbols) of the kernel symbol table.

## Profiling setup

if profiling ("profile=#" on the kernel command line): calculate the kernel text (code) profile "segment" size; calculate the profile buffer size in pages (round up); allocate the profile buffer: `prof_buffer = alloc_bootmem(size)`;

## kmem\_cache\_init

(in `linux/mm/slab.c`)

### sti

\*\*\*\*\* Interrupts are now enabled. \*\*\*\*\*

This allows "calibrate\_delay()" (below) to work.

## calibrate\_delay

Calculate the "loops\_per\_jiffy" delay loop value and print it in BogoMIPS.

## INITRD setup

```
#ifdef CONFIG_BLK_DEV_INITRD
    if (initrd_start && !initrd_below_start_ok &&
        initrd_start < (min_low_pfn << PAGE_SHIFT)) {
        printk("initrd overwritten (initrd_start < (min_low_pfn << PAGE_SHIFT)) -
            initrd_start = 0;          // mark initrd as disabled
        }
#endif /* CONFIG_BLK_DEV_INITRD */
```

## mem\_init

(in `linux/arch/i386/mm/init.c`)

- Clear the `empty_zero_page`.
- Call `free_all_bootmem()` and add that released memory to `totalram_pages`.
- Count the number of reserved RAM pages.
- Print the system memory sizes (free/total), kernel code size, reserved memory size, kernel data size, kernel "init" size, and the highmem size.
- For CONFIG\_SMP, call `zap_low_mappings()`.

\*\*\*\*\* `get_free_pages()` can be used after `mem_init()`. \*\*\*\*\*

## kmem\_cache\_sizes\_init

(in `linux/mm/slab.c`)

Set up remaining internal and general caches. Called after the "get\_free\_page()" functions have been enabled and before smp\_init().

\*\*\*\*\* kmalloc() can be used after kmem\_cache\_sizes\_init(). \*\*\*\*\*

## **proc\_root\_init**

(in linux/fs/proc/root.c)

For CONFIG\_PROC\_FS configurations:

- call proc\_misc\_init()
- mkdir /proc/net
- for CONFIG\_SYSVIPC, mkdir /proc/sysvipc
- for CONFIG\_SYSCTL, mkdir /proc/sys
- mkdir /proc/fs
- mkdir /proc/driver
- call proc\_tty\_init()
- mkdir /proc/bus

**mempages = num\_physpages;**

## **fork\_init(mempages)**

(in linux/kernel/fork.c)

The default maximum number of threads is set to a safe value: the thread structures can take up at most half of memory.

## **proc\_caches\_init()**

(in linux/kernel/fork.c)

Call kmem\_cache\_create() to create slab caches for signal\_act (signal action), files\_cache (files\_struct), fs\_cache (fs\_struct), vm\_area\_struct, and mm\_struct.

## **vfs\_caches\_init(mempages)**

(in linux/fs/dcache.c)

Call kmem\_cache\_create() to create slab caches for buffer\_head, names\_cache, filp, and for CONFIG\_QUOTA, dquot.

Call dcache\_init() to create the dentry\_cache and dentry\_hashtable.

## **buffer\_init(mempages)**

(in linux/fs/buffer.c)

Allocate the buffer cache hash table and init the free list.  
Use `get_free_pages()` for the hash table to decrease TLB misses; use SLAB cache for buffer heads.  
Setup the hash chains, free lists, and LRU lists.

## **page\_cache\_init(mempages)**

(in `linux/mm/filemap.c`)

Allocate and clear the page-cache hash table.

## **kiobuf\_setup()**

(in `linux/fs/iobuf.c`)

Call `kmem_cache_create()` to create the kernel iobuf cache.

## **signals\_init()**

(in `linux/kernel/signal.c`)

Call `kmem_cache_create()` to create the "sigqueue" SLAB cache.

## **bdev\_init()**

(in `linux/fs/block_dev.c`)

Initialize the `bdev_hashtable` list heads.

Call `kmem_cache_create()` to create the "bdev\_cache" SLAB cache.

## **inode\_init(mempages)**

(in `linux/fs/inode.c`)

- Allocate memory for the `inode_hashtable`.
- Initialize the `inode_hashtable` list heads.
- Call `kmem_cache_create()` to create the inode SLAB cache.

## **ipc\_init()**

(in `linux/ipc/util.c`)

For `CONFIG_SYSVIPC` configurations, call `ipc_init()`.

The various System V IPC resources (semaphores, messages, and shared memory) are initialized.

## dquot\_init\_hash()

(in linux/fs/dquot.c)

For CONFIG\_QUOTA configurations, call dquot\_init\_hash().

- Clear dquot\_hash. TBD: Why? Is it in BSS? Yes.
- Clear dqstats. TBD: Why? Is it in BSS? Yes.

## check\_bugs()

(in linux/include/asm-i386/bugs.h)

- identify\_cpu()
- For non-CONFIG\_SMP configurations, print\_cpu\_info()
- check\_config()
- check\_fpu()
- check\_hlt()
- check\_popad()
- Update system\_utsname.machine{byte 1} with boot\_cpu\_data.x86

## Start other SMP processors (as applicable)

smp\_init() works in one of three ways, depending upon the kernel configuration.

For a uniprocessor (UP) system without an IO APIC (CONFIG\_X86\_IO\_APIC is not defined), smp\_init() is empty — it has nothing to do.

For a UP system with (an) IO APIC for interrupt routing, it calls IO\_APIC\_init\_uniprocessor().

For an SMP system, its main job is to call the architecture-specific function "smp\_boot\_cpus()", which does the following.

- For CONFIG\_MTRR kernels, calls mtrr\_init\_boot\_cpu(), which must be done before the other processors are booted.
- Stores and prints the BSP CPU information.
- Saves the BSP APIC ID and BSP logical CPU ID (latter is 0).
- If an MP BIOS interrupt routing table was not found, revert to using only one CPU and exit.
- Verify existence of a local APIC for the BSP.
- If the "maxcpus" boot option was used to limit the number of CPUs actually used to 1 (not SMP), then ignore the MP BIOS interrupt routing table.
- Switch the system from PIC mode to symmetric I/O interrupt mode.
- Setup the BSP's local APIC.
- Use the CPU present map to boot the APs serially. Wait for each AP to finish booting before starting the next one.
- If using (an) IO APIC {which is True unless the "noapic" boot option was used}, setup the IO APIC(s).

## Start init thread

We count on the initial thread going OK.

Like idlers, `init` is an unlocked kernel thread, which will make syscalls (and thus be locked).

```
kernel_thread(init, NULL, CLONE_FS | CLONE_FILES | CLONE_SIGNAL);
```

{details below}

## unlock\_kernel()

Release the BKL.

```
current->need_resched = 1;
```

## cpu\_idle()

This function remains as process number 0. Its purpose is to use up idle CPU cycles. If the kernel is configured for APM support or ACPI support, `cpu_idle()` invokes the supported power-saving features of these specifications. Otherwise it nominally executes a "hlt" instruction.

{end of `start_kernel()`}

## 5.2 setup\_arch

(in "linux/arch/i386/kernel/setup.c")

### Copy and convert system parameter data

Copy and convert parameter data passed from 16-bit real mode to the 32-bit startup code.

### For RAMdisk-enabled configs (CONFIG\_BLK\_DEV\_RAM)

Initialize `rd_image_start`, `rd_prompt`, and `rd_doload` from the real-mode parameter data.

### setup\_memory\_region

Use the BIOS-supplied memory map to setup memory regions.

### Set memory limits

Set values for the start of kernel code, end of kernel code, end of kernel data, and "`_end`" (end of kernel code = the "`brk`" address).

Set values for `code_resource` start and end and `data_resource` start and end.

## **parse\_mem\_cmdline**

Parse any "mem=" parameters on the kernel command line and remember them.

## **Setup Page Frames**

Use the BIOS-supplied memory map to setup page frames.

Register available low RAM pages with the bootmem allocator.

Reserve physical page 0: "it's a special BIOS page on many boxes, enabling clean reboots, SMP operation, laptop functions."

## **Handle SMP and IO APIC Configurations**

For CONFIG\_SMP, reserve the page immediately above page 0 for stack and trampoline usage, then call `smp_alloc_memory()` to allocate low memory for AP processor(s) real mode trampoline code.

For CONFIG\_X86\_IO\_APIC configurations, call `find_smp_config()` to find and reserve any boot-time SMP configuration information memory, such as MP (Multi Processor) table data from the BIOS.

## **paging\_init()**

`paging_init()` sets up the page tables – note that the first 8 MB are already mapped by `head.S`.

This routine also unmaps the page at virtual kernel address 0, so that we can trap those pesky NULL-reference errors in the kernel.

## **Save the boot-time SMP configuration**

For CONFIG\_X86\_IO\_APIC configurations, call `get_smp_config()` to read and save the MP table IO APIC interrupt routing configuration data.

For CONFIG\_X86\_LOCAL\_APIC configurations, call `init_apic_mappings()`.

## **Reserve INITRD memory**

For CONFIG\_BLK\_DEV\_INITRD configurations, if there is enough memory for the initial RamDisk, call `reserve_bootmem()` to reserve RAM for the initial RamDisk.

## **Scan for option ROMs**

Call `probe_roms()` and reserve their memory space resource(s) if found and valid. This is done for the standard video BIOS ROM image, any option ROMs found, and for the system board extension ROM (space).

## Reserve system resources

Call `request_resource()` to reserve video RAM memory.

Call `request_resource()` to reserve all standard PC I/O system board resources.

{end of `setup_arch()`}

## 5.3 init thread

The init thread begins at the `init()` function in "linux/init/main.c". This is always expected to be process number 1.

`init()` first locks the kernel and then calls `do_basic_setup()` to perform lots of bus and/or device initialization {more detail below}. After `do_basic_setup()`, most kernel initialization has been completed. `init()` then frees any memory that was specified as being for initialization only [marked with "`__init`", "`__initdata`", "`__init_call`", or "`__initsetup`"] and unlocks the kernel (BKL).

`init()` next opens `/dev/console` and duplicates that file descriptor two times to create `stdin`, `stdout`, and `stderr` files for `init` and all of its children.

Finally `init()` tries to execute the command specified on the kernel parameters command line if there was one, or an `init` program or script if it can find one in `{/sbin/init, /etc/init, /bin/init}`, and lastly `/bin/sh`. If `init()` cannot execute any of these, it panics ("No `init` found. Try passing `init=` option to kernel.")

## 5.4 do\_basic\_setup {part of the init thread}

The machine is now initialized. None of the devices have been touched yet, but the CPU subsystem is up and running, and memory and process management works.

### Be the reaper of orphaned children

The `init` process handles all orphaned tasks.

### MTRRs

// SMP `init` is completed before this.

For `CONFIG_MTRR`, call `mtrr_init()` [in `linux/arch/i386/kernel/mtrr.c`].

### SYSCTLs

For `CONFIG_SYSCTL` configurations, call `sysctl_init()` [in `linux/kernel/sysctl.c`].

### Init Many Devices

```
/*
 * Ok, at this point all CPU's should be initialized, so
 * we can start looking into devices..
 */
```

## PCI

For CONFIG\_PCI configurations, call `pci_init()` [in `linux/drivers/pci/pci.c`].

## Micro Channel

For CONFIG\_MCA configurations, call `mca_init()` [in `linux/arch/i386/kernel/mca.c`].

## ISA PnP

For CONFIG\_ISAPNP configurations, call `isapnp_init()` [in `linux/drivers/pnp/isapnp.c`].

## Networking Init

```
/* Networking initialization needs a process context */
sock_init();
```

[in `linux/net/socket.c`]

## Initial RamDisk

```
#ifdef CONFIG_BLK_DEV_INITRD

    real_root_dev = ROOT_DEV;
    real_root_mountflags = root_mountflags;
    if (initrd_start && mount_initrd)
        root_mountflags &= ~MS_RDONLY;    // change to read/write
    else
        mount_initrd = 0;

#endif /* CONFIG_BLK_DEV_INITRD */
```

## Start the kernel "context" thread (keventd)

[in `linux/kernel/context.c`]

## Initcalls

Call all functions marked as "`__initcall`":

```
do_initcalls();
```

[in `linux/init/main.c`]

This initializes many functions and some subsystems — in no specific or guaranteed order unless fixed in their Makefiles — if they were built into the kernel, such as:

- APM: `apm_init()` {in `linux/arch/i386/kernel/apm.c`}
- cpuid: `cpuid_init()` {in `linux/arch/i386/kernel/cpuid.c`}
- DMI: `dmi_scan_machine()` {in `linux/arch/i386/kernel/dmi_scan.c`}
- microcode: `microcode_init()` {in `linux/arch/i386/kernel/microcode.c`}

- MSR: `msr_init()` {in `linux/arch/i386/kernel/msr.c`}
- partitions: `partition_setup()` {in `linux/fs/partitions/check.s`}
- file systems, pipes, buffer and cache management, various binary format loaders, NLS character sets: too numerous to list {in `linux/fs/*`}
- user cache (for limits): `uid_cache_init()` {in `linux/kernel/user.c`}
- `kmem_cpu_cache`: `kmem_cpucache_init()` {in `linux/mm/slab.c`}
- `shmem`: `init_shmem_fs()` {in `linux/mm/shmem.c`}
- `kswapd`: `kswapd_init()` {in `linux/mm/vmscan.c`}
- networking, TCP/IP, IPv6, sockets, 802.2, SNAP, LLC, X.25, AX.25, IPX, kHTTPd, ATM LAN emulation (LANE), IP chains/forwarding, NAT/masquerading, packet matching/filtering/logging, firewalling, DECnet, bridging, and other networking protocols too numerous to list {in `linux/net/*`}
- drivers, some of which are not exactly device drivers, but help out with bus/device enumeration and initialization, such as:
  - ACPI: `acpi_init()` {in `linux/drivers/acpi/*`}
  - PCI: `pci_proc_init()` {in `linux/drivers/pci/*`}
  - PCMCIA controllers {in `linux/drivers/pcmcia/*`}
  - and...
  - atm drivers {in `linux/drivers/atm/*`}
  - block drivers {in `linux/drivers/block/*`}
  - CD-ROM drivers {in `linux/drivers/cdrom/*`}
  - character drivers {in `linux/drivers/char/*`}
  - I2O drivers {in `linux/drivers/i2o/*`}
  - IDE drivers {in `linux/drivers/ide/*`}
  - input drivers (keyboard/mouse/joystick) {in `linux/drivers/input/*`}
  - ISDN drivers {in `linux/drivers/isdn/*`}
  - md, LVM, and RAID drivers {in `linux/drivers/md/*`}
  - radio drivers {in `linux/drivers/media/radio/*`}
  - video drivers {in `linux/drivers/media/video/*`}
  - MTD drivers {in `linux/drivers/mtd/*`}
  - network drivers, including PLIP, PPP, dummy, Ethernet, bonding, Arcnet, hamradio, PCMCIA, Token Ring, and WAN
  - SCSI logical and physical drivers {in `linux/drivers/scsi/*`}
  - sound drivers {in `linux/drivers/sound/*`}
  - telephony drivers {in `linux/drivers/telephony/*`}
  - USB host controllers and device drivers {in `linux/drivers/usb/*`}
  - video frame buffer drivers {in `linux/drivers/video/*`}

## Filesystems

Call `filesystem_setup()`:

- `init_devfs_fs()`; /\* Header file may make this empty \*/
- For `CONFIG_NFS_FS` configurations, call `init_nfs_fs()`.
- For `CONFIG_DEVPTS_FS` configurations, call `init_devpts_fs()`.

[in `linux/fs/filesystems.c`]

## IRDA

For `CONFIG_IRDA` configurations, call `irda_device_init()`.

/\* Must be done after protocol initialization \*/

[in linux/net/irda/irda\_device.c]

## PCMCIA

/\* Do this last \*/

For CONFIG\_PCMCIA configurations, call init\_pcmcia\_ds().

[in linux/drivers/pcmcia/ds.c]

## Mount the root filesystem

```
mount_root();
```

[in linux/fs/super.c]

## Mount the dev (device) filesystem

```
mount_devfs_fs ();
```

[in linux/fs/devfs/base.c]

## Switch to the Initial RamDisk

```
#ifdef CONFIG_BLK_DEV_INITRD

    if (mount_initrd && MAJOR(ROOT_DEV) == RAMDISK_MAJOR && MINOR(ROOT_DEV) == 0) {
        // Start the linuxrc thread.
        pid = kernel_thread(do_linuxrc, "/linuxrc", SIGCHLD);
        if (pid > 0)
            while (pid != wait(&i));
        if (MAJOR(real_root_dev) != RAMDISK_MAJOR
            || MINOR(real_root_dev) != 0) {
            error = change_root(real_root_dev, "/initrd");
            if (error)
                printk(KERN_ERR "Change root to /initrd: "
                       "error %d\n", error);
        }
    }

#endif /* CONFIG_BLK_DEV_INITRD */
```

See "linux/Documentation/initrd.txt" for more information on initial RAM disks.

{end of do\_basic\_setup()}

---

## 6. [Glossary](#)

AP: Application Processor, any x86 processor other than the Bootstrap Processor on IA-32 SMP systems

ACPI: Advanced Configuration and Power Interface

APIC: Advanced Programmable Interrupt Controller

APM: Advanced Power Management, a BIOS-managed power management specification for personal computers

BSP: Bootstrap Processor, the primary booting processor on IA-32 SMP systems

BSS: Block Started by Symbol: the uninitialized data segment

BKL: Big Kernel Lock, the Linux global kernel lock

CRn: Control Register n, i386-specific control registers

FPU: Floating Point Unit, a separate math coprocessor device

GB: gigabyte (1024 \* 1024 \* 1024 bytes)

GDT: Global Descriptor Table, an i386 memory management table

IA: Intel Architecture (also i386, x86)

IDT: Interrupt Descriptor Table, an i386-specific table that contains information used in handling interrupts

initrd: initial RAM disk (see "linux/Documentation/initrd.txt")

IPC: Inter-Process Communication

IPI: Inter-processor Interrupt, a method of signaling interrupts between multiple processors on an SMP system

IRDA: InfraRed Data Association

IRQ: Interrupt ReQuest

ISA: Industry Standard Architecture

KB: kilobyte (1024 bytes)

LDT: Local Descriptor Table, an i386-specific memory management table that is used to describe memory for each non-kernel process

MB: megabyte (1024 \* 1024 bytes)

MCA: Micro Channel Architecture, used in IBM PS/2 computers

MP: Multi-processor

MSW: Machine Status Word

MTRR: Memory Type Range Registers

PAE: Physical Address Extension: extends the address space to 64 GB instead of 4 GB

PCI: Peripheral Component Interconnect, an industry standard for connecting devices on a local bus in a computer system

PCMCIA: Personal Computer Memory Card International Association; defines standards for PCMCIA cards and CardBus PC Cards

PIC: Programmable Interrupt Controller

PNP: Plug aNd Play

PSE: Page Size Extension: allows 4 MB pages

SMP: Symmetric Multi Processor/Processing

TLB: Translation Lookaside Buffer, i386-specific processor cache of recent page directory and page table entries

TSS: Task State Segment, an i386-specific task data structure

UP: Uniprocessor (single CPU) system.

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## 7. [References](#)

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